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- +Microprocessor Boards
- +FMC Cards -Peripheral Modules Anvyl Pmod Pack Cable Kit, 12-pin Cable Kit, 6-pin Pmod8LD PmodACL PmodACL2
- PmodAD1 PmodAD2 PmodAD5 PmodALS
- PmodAMP2 PmodAMP3 PmodBB PmodBT2
- **PmodBTN** PmodCDC1 PmodCLIP **PmodCLP PmodCLS**
- PmodCMPS PmodCON1 PmodCON3
- PmodCON4 PmodDA1 PmodDA2
- PmodDA3 PmodDA4 PmodDHB1
- PmodDIP PmodDPOT **PmodENC**
- PmodGPS PmodGYRO PmodHB3
- PmodHB5 PmodI2S
- PmodIA PmodIOXP
- PmodJSTK PmodKYPD PmodLED
- PmodLS1 PmodLVLSHFT PmodMAXSONAR
- PmodMIC3 PmodNIC100
- PmodOC1 PmodOD1 PmodOLED
- PmodPMON1 PmodPS2 PmodR2R
- PmodRF2 PmodRJ45
- PmodRS232 PmodRS485 PmodRTCC
- PmodSD PmodSF PmodSF2
- PmodSSD PmodSTEP
- **PmodSWT** PmodTC1 PmodTMP2
- PmodTMP3 PmodTPH PmodTPH2 **PmodUSBUART**
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PmodHB5 - H-bridge w/ feedback inputs

Part # 410-106P

\$28.00 Add to Cart

Currently in stock

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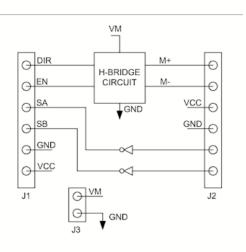
Connector(s):

6-pin header and 6-pin JST connector

- 2A H-bridge circuit capable of handling up to 12V
- Two feedback channels w/ Schmitt-trigger inputs
- Small size (0.8" X 1.3")
- Ships with a 6" 6-pin cable and a 6-pin header

The Digilent PmodHB5 2A H-bridge module is an ideal solution for robotics and other applications where logic signals are used to drive small to medium-sized DC motors such as the Digilent motor/gearbox.





Support Documents:

Doc#	Date	Categories	Description	
DSD-0000084	2/29/12	PD	PmodHB5 Reference Manual	Download
DSD-0000102		PD	PmodHB5 Schematics	Download
DSD-0000226	4/20/09	RD	PmodHB5 reference component	Download
DSD-0000227	4/20/09	RD	PmodHB5 example reference design	Download
DSD-0000355	2/29/12	DP	This zip file contains a library and MPLAB demo project for the PmodHB5. This project was made for MPLAB v8.83, targets the Cerebot MX4cK, and is written in C.	Download
DSD-0000358	2/29/12	DP	This zip file contains a Xilinx ISE demo project for the PmodHB5. This project was made for Xilinx ISE 13.4, targets the Nexys3, and is written in VHDL.	Download
DSD-0000391	9/07/12	DP	This zip file contains a Xilinx ISE demo project for the PmodHB5. This project was made for Xilinx ISE 14.2, targets the Nexys3, and is written in VHDL.	Download
DSD-0000392	9/07/12	DP	This zip file contains a Xilinx ISE demo project for the PmodHB5. This project was made for Xilinx ISE 14.2, targets the Nexys3, and is written in Verilog.	Download
DSD-0000459	6/09/14	DP	This MPIDE library simplifies the use of th PmodHB5 with our motors by providing simple functions using the input capture interrupts for motor feedback. The included demo initializes the input capture interrupts and allows manipulation of the wheel direction through button presses.	Download

